

ABSTRACT OF THE DISCLOSURE

There is provided an information processing method which enhances the executing speed of a parallel processing computing device. A parallel processing block-forming step divides a program to be executed into a plurality of parallel processing blocks. A thread-forming step divides parallel processing blocks formed by the parallel processing block-forming step into a plurality of threads corresponding in number to the processors of a processor group. The processor group executes the threads generated by the thread-forming step. The instructing step instructs each of the processors to execute a thread of a next parallel processing block when it has terminated execution of a thread assigned thereto, and when a predetermined instruction is not given in the program.